

A_S-2W & B_S-2W Series

2W, FIXED INPUT, ISOLATED & UNREGULATED DUAL/SINGLE OUTPUT DC-DC CONVERTER



RoHS (C. This

FEATURES

High Efficiency up to 86%
1KVDC Isolation
SIP Package
Internal SMD Construction
Temperature Range: -40°C to +85°C
No Heat sink Required
No External Component Required
Industry Standard Pinout
RoHS Compliance

APPLICATIONS

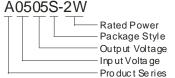
The A_S-2W & B_S-2W Series are specially designed for applications where a group of polar power supplies are isolated from the input power supply in a distributed power supply system on a circuit board.

These products apply to:

- Where the voltage of the input power supply is fixed (voltage variation ≤ ±10%);
- Where isolation is necessary between input and output (isolation voltage ≤1000VDC);
- Where the regulation of the output voltage and the output ripple noise are not demanding.

Such as: purely digital circuits, ordinary low frequency analog circuits, and IGBT power device driving circuits.

MODEL SELECTION



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PRODUCT PROGRAM							
Dt	In	put (Output		Γ#:.:	UL CE
Part Number	Voltage (VDC)		Voltage	Current (mA)		Efficiency (%, Typ)	
Number	Nominal	Range	ge (VDC) _{Max} M		Min	(70, 199)	
B0303S-2W	3.3	2.97-3.63	3.3	400	40	73	
A0505S-2W			±5	±200	±20	82	UL
A0509S-2W		4.5-5.5	±9	±111	±12	85	UL
A0512S-2W			±12	±83	±9	86	UL
A0515S-2W			±15	±67	±7	82	UL
A0524S-2W	5		±24	±42	±5	82	
B0503S-2W	5		3.3	400	40	74	
B0505S-2W			5	400	40	81	UL CE
B0509S-2W			9	222	23	84	UL CE
B0512S-2W			12	167	17	83	UL CE
B0515S-2W			15	133	14	84	UL CE
A1205S-2W		10.8-13.2	±5	±200	±20	81	UL
A1209S-2W			±9	±111	±12	84	UL
A1212S-2W	- 700		±12	±83	±9	86	UL
A1215S-2W	12		±15	±67	±7	82	UL
B1205S-2W	12		5	400	40	81	UL CE
B1209S-2W	L'YOU		9	222	23	82	UL CE
B1212S-2W	N. N		12	167	17	85	UL CE
B1215S-2W			15	133	14	82	UL CE
A1505S-2W	15	13.5-16.5	±5	±200	±20	80	
B1505S-2W	15	13.5-16.5	±5	400	40	80	
A2405S-2W			±5	±200	±20	80	UL
A2409S-2W			±9	±111	±12	84	UL
A2412S-2W			±12	±83	±9	84	UL
A2415S-2W			±15	±67	±7	84	UL
A2424S-2W	24	04.0.00.4	±24	±42	±5	85	
B2405S-2W	24	21.6-26.4	5	400	40	80	UL CE
B2409S-2W			9	222	23	83	UL CE
B2412S-2W			12	167	17	84	UL CE
B2415S-2W			15	133	14	84	UL CE
B2424S-2W]		24	84	10	84	
Note:the A S 1W/B	LS 1W series	also are availab	ole in our comp	anv.			

Note: the A_S_1W/B_LS_1W series also are available in our company.

Item	Test conditions	Min	Тур	Max	Units
	Test conditions		199		OTING
Operating Temp. Range		-40		85	°C
Storage Temp. Range		-55		125	
Storage humidity range				95	%
Cooling			Free air convection		
Temp. rise at full load			15	25	· °C
Lead temperature	1.5mm from case for 10 seconds			300	
Short circuit protection*				1	s
Case material		Plastic (UL94-V0))
MTBF		3500			K hours
Weight			2.8		g

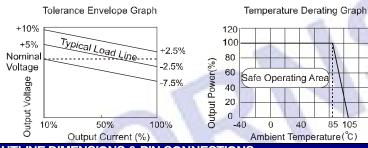
ISOLATION SPECIFICATIONS						
Item	Test conditions	Min	Тур	Max	Units	
Isolation voltage	Tested for 1 minute and 1 mA max	1000			VDC	
Isolation resistance	Test at 500VDC	1000			ΜΩ	

Test conditions		Min	Тур	Max	Units
- \"				ax	Units
- \"		0.2		2	W
For Vin change of 1%	For Vin change of 1%			±1.2	
10% to 100% load	(5V output)		12.8	15	%
	(9V output)		8.3	15	
	(12V output)		6.8	15	
	(15V output)		6.3	15	
	(24V output)		5	15	
acy		See to	lerance e	nvelope g	raph
100% full load			0.03	%/°C	
20MHz Bandwidth		75	150	mVp-p	
vitching frequency Full load, nominal input			75		KHz
	10% to 100% load acy 100% full load 20MHz Bandwidth Full load, nominal in	(5V output) (9V output) (12V output) (15V output) (24V output) (24V output) (20MHz Bandwidth Full load, nominal input	(5V output) (9V output) (10% to 100% load (12V output) (15V output) (24V output) acy See to 100% full load 20MHz Bandwidth Full load, nominal input	(5V output) 12.8 (9V output) 8.3 (12V output) 6.8 (15V output) 6.3 (24V output) 5 (24V output) 5 (24V output) 75 (24V output) (24	(5V output) 12.8 15 (9V output) 8.3 15 (10% to 100% load (12V output) 6.8 15 (15V output) 6.3 15 (24V output) 5 15 (24V output) 5 (24V output) 6 (24V output) 5 (24V output) 6 (24V output) (24V output) (24V output) (24V output) (24V output) (24V output) (2

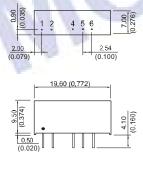
*Test ripple and noise by "parallel cable" method. See detailed operation instructions at Testing of Power Converter section, application notes.

- 1.All specifications measured at $T_A=25$ °C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
- 2.Dual output models unbalanced load: ±5%.

TYPICAL CHARACTERISTICS

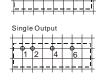


OUTLINE DIMENSIONS & PIN CONNECTIONS



First Angle Projection 🖯 🏶 RECOMMENDED FOOTPRINT Top view,grid:2.54*2.54mm(0 diameter:1.00mm(0.039inch) (0.1*0.1inch) Dual Output

85 105



FOOTFRINT DE IAILS				
Pin	Singles	Duals		
1	Vin	Vin		
2	GND	GND		
4	0V	-V0		
5	No Pin	0V		
6	+Vo	+Vo		

APPLICATION NOTE

Requirement on output load

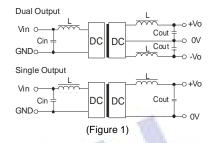
To ensure this module can operate efficiently and reliably, During operation, the minimum output load is not less than 10% of the full load, and that this product should never be operated under no load! If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, or use our company's products with a lower rated output power. (A_S-1W /B_LS-1W Series) .

Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

Recommended testing and application circuit

If you want to further decrease the input/output ripple, an "LC" filtering network may be connected to the input and output ends of the DC/DC converter, see (Figure 1).



It should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees (Table 1).

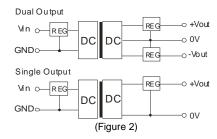
EXTERNAL CAPACITOR TABLE (Table 1)

Vin (VDC)	Cin (uF)	Vout (VDC)	Cout (uF)	Dual Vout (VDC)	Cou (uF)
5	4.7	5	10	±5	4.7
12	2.2	9	4.7	±9	2.2
15	2.2	12	2.2	±12	1
24	1	15	1	±15	0.47

It's not recommend to connect any external capacitor in the application field with less than 0.5 watt output.

Output Voltage Regulation and Over-voltage **Protection Circuit**

The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (Figure 2).



No parallel connection or plug and play.